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OTHER PRIOR ART (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

BR	AR	Chin et al., A 10b 125-MHz CMOS Digital-to-Analog Converter (DAC) with Threshold-Voltage Compensated Current Sources, IEEE Journal of Solid-State Circuits, Vol. 29, No. 11 (1994), pp. 1374-1380
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JBT2	AR	Chin et al., A 10b 125-MHz CMOS Digital-to-Analog Converter (DAC) with Threshold-Voltage Compensated Current Sources, IEEE Journal of Solid-State Circuits, Vol. 29, No. 11 (1994), pp. 1374-1380
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